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PATENT  
Attorney Docket No.: SONY-14700

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Mark K. Eyer

Serial No.: 09/904,973

Filed: July 12, 2001

For: **CABLE AND CONNECTION WITH  
INTEGRATED DVI AND IEEE 1394  
CAPABILITIES**

Group Art Unit:

Examiner:

**TRANSMITTAL LETTER**

260 Sheridan Avenue, Suite 420  
Palo Alto, CA 94306  
(650) 833-0160

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SEP 12 2001

Technology Center 2600

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Enclosed please find an Information Disclosure Statement, Form PTO-1449, including copies of the references contained thereon, for filing in the U.S. Patent and Trademark Office.

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. 08-1275. **An originally executed duplicate of this transmittal is enclosed for this purpose.**

Respectfully submitted,  
HAVERSTOCK & OWENS LLP

Dated: August 15, 2001

By: Jonathan O. Owens  
Jonathan O. Owens  
Reg. No.: 37,902

Attorneys for Applicant

CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington D.C. 20231

- 1 -

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HAVERSTOCK & OWENS LLP

Date: 8/15/01 By: Jonathan A. Pearson



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) Group Art Unit:

) Examiner:

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) **INFORMATION DISCLOSURE  
STATEMENT**

) 260 Sheridan Avenue, Suite 420  
) Palo Alto, California 94306  
) (650)833-0160

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicant has become aware of the following printed publication which may be material to the examination of this application:

- U.S. Patent No. 2,386,753;
- U.S. Patent No. 2,603,684;
- U.S. Patent No. 3,785,432;
- U.S. Patent No. 4,376,920;
- U.S. Patent No. 4,604,689;
- U.S. Patent No. 4,761,519;
- U.S. Patent No. 4,763,360;
- U.S. Patent No. 4,822,304;
- U.S. Patent No. 4,842,366;
- U.S. Patent No. 4,853,555;
- U.S. Patent No. 4,871,883;

CERTIFICATE OF MAILING (37 CFR § 1.8(a))

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HAVERSTOCK & OWENS LLP.

Date: 8/15/01 By: John D. Rawson

- U.S. Patent No. 4,881,244;
- U.S. Patent No. 4,924,037;
- U.S. Patent No. 4,979,185;
- U.S. Patent No. 5,055,064;
- U.S. Patent No. 5,133,034;
- U.S. Patent No. 5,162,609;
- U.S. Patent No. 5,216,202;
- U.S. Patent No. 5,216,204;
- U.S. Patent No. 5,244,415;
- U.S. Patent No. 5,362,249;
- U.S. Patent No. 5,400,340;
- U.S. Patent No. 5,412,697;
- U.S. Patent No. 5,418,478;
- U.S. Patent No. 5,483,656;
- U.S. Patent No. 5,485,458;
- U.S. Patent No. 5,485,488;
- U.S. Patent No. 5,493,657;
- U.S. Patent No. 5,499,344;
- U.S. Patent No. 5,500,946;
- U.S. Patent No. 5,504,458;
- U.S. Patent No. 5,504,757;
- U.S. Patent No. 5,509,126;
- U.S. Patent No. 5,527,996;
- U.S. Patent No. 5,572,658;
- U.S. Patent No. 5,574,250;
- U.S. Patent No. 5,579,486;
- U.S. Patent No. 5,592,510;
- U.S. Patent No. 5,619,544;
- U.S. Patent No. 5,754,548;
- U.S. Patent No. 5,781,028;
- U.S. Patent No. 5,796,042;
- U.S. Patent No. 5,808,660;

- U.S. Patent No. 5,881,249;
- U.S. Patent No. 5,945,631;
- "1394 200 Mb/s PHYsical Layer Transceiver," IBM Microelectronics, Product Data Sheet and Application Notes, Version 1.4, 3/14/96;
- "IEEE 1394-1995 TRIPLE CABLE TRANSRECEIVER/ ARBITER," Texas Instruments, TSB21LV03, Product Preview, Revision 0.99, 3/19/96;
- "P1394 Standard for a High Performance Serial Bus," IEEE P1394 Draft 8.0v2, July 7, 1995;
- Tensolite Company product specification, part number 20470/9J207X-4(LD);
- Tensolite Company product specification, part number 18480/9J207X-4(LD);
- Tensolite Company product specification, part number 24443/9B048X-4(LD) 6/3/93;
- Tensolite Company product specification, part number 24443/9C062X-4(LD), 3/17/93;
- Craig Theorin, "High speed serial links benefit from advanced cabling," 10/26/95;
- Raychem specification control drawing, part number EPD-RWC-13458, 8/7/95;
- Raychem specification control drawing, part number 82A0111, 9/10/95, page 1 of 2;
- Michael Teener et al., "A Bus on a Diet - The Serial Bus Alternative, An Introduction to the P1394 High performance Serial Bus" Apple Computer, Inc. Santa Clara, CA, Pub. Date.: 02/24/92, pgs. 316-321;
- "The IEEE-1394 High Speed Serial Bus," R.H.J. Bloks, Philips Journal Of Research, Vol.50, No. 1/2, pp. 209-216, 1996;
- P1394a Draft Standard For A High Performance Serial Bus (Supplement), P1394a Draft 2.0 March 15, 1998; and
- "Digital Visual Interface - DVI" DDWG, Revision 1.0, April 2, 1999, page 1 of 76.

This Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,  
HAVERSTOCK & OWENS LLP

Dated: August 15, 2001

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Attorneys for Applicant

FORM PTO-1449  
(Modified)U.S. Department of Commerce  
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Serial No.: 09/904,973

**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**  
(Use Several Sheets If Necessary)

(37 CFR § 1.98(b))

Applicant: Mark K. Eyer

Filing Date: July 12, 2001

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Group Art Unit

## U.S. PATENT DOCUMENTS

SEP 12 2001

Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Technology Center	Pub Date
	AA	2,386,753	10/16/45	J. Shield	174	36	10/03/42
	AB	2,603,684	07/15/52	E.P. Holmes	174	106	07/20/48
	AC	3,785,432	01/15/74	Kabat et al.	165	22	10/02/72
	AD	4,376,920	03/15/83	Smith	333	12	04/01/81
	AE	4,604,689	08/05/86	Burger	364	200	04/15/83
	AF	4,761,519	08/02/88	Olson et al.	174	107	01/29/87
	AG	4,763,360	08/09/88	Daniels et al.	455	3	09/17/86
	AH	4,822,304	04/18/89	Herron	439	610	09/24/87
	AI	4,842,366	06/27/89	Sawada et al.	350	96.30	03/03/88
	AJ	4,853,555	08/01/89	Wheat	307	9.1	04/21/88
	AK	4,871,883	10/03/89	Guiol	174	36	07/23/87
	AL	4,881,244	11/14/89	Haug	375	36	12/11/87
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	AN	4,979,185	12/18/90	Bryans et al.	375	20	10/30/89
	AO	5,055,064	10/08/91	Imaizumi et al.	439	402	02/04/91
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	AQ	5,162,609	11/10/92	Adriaenssens et al.	174	34	07/31/91
	AR	5,216,202	06/01/93	Yoshida et al.	174	36	08/21/91
	AS	5,216,204	06/01/93	Dudek et al.	174	102	08/02/91
	AT	5,244,415	09/14/93	Marsilio et al.	439	610	02/07/92
	AU	5,362,249	11/08/94	Carter	439	357	05/04/93
	AV	5,400,340	03/21/95	Hillman et al.	370	105.3	03/04/93
	AW	5,412,697	05/02/95	Van Brunt et al.	375	360	01/14/93
	AX	5,418,478	05/23/95	Van Brunt et al.	326	86	07/30/93
	AY	5,483,656	01/09/96	Oprescu et al.	395	750	01/14/93
	AZ	5,485,458	01/16/96	Oprescu et al.	370	85.2	03/05/93
	BA	5,485,488	01/16/96	Van Brunt et al.	375	257	03/29/94
	BB	5,493,657	02/20/96	Van Brunt et al.	395	308	06/21/93
	BC	5,499,344	03/12/96	Elnashar et al.	395	250	10/07/92
	BD	5,500,946	03/19/96	Roden et al.	395	308	01/27/95
	BE	5,504,458	04/02/96	Van Brunt et al.	330	255	09/30/94
	BF	5,504,757	04/02/96	Cook et al.	370	84	09/27/94
	BG	5,509,126	04/16/96	Oprescu et al.	395	307	03/16/93
	BH	5,527,996	06/18/96	Ham	174	113 R	06/17/94
	BI	5,572,658	11/05/96	Mohr et al.	395	182.02	08/05/93
	BJ	5,574,250	11/12/96	Hardie et al.	174	36	02/03/95

Examiner:

Date Considered:

**EXAMINER:**

Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449  
(Modified)U.S. Department of Commerce  
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Attorney Docket No.: SONY-14700

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Applicant: Mark K. Eyer

(37 CFR § 1.98(b))

Filing Date: July 12, 2001

Group Art Unit:

## U.S. PATENT DOCUMENTS

Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date
	BK	5,579,486	11/26/96	Oprescu et al.	395	200.15	01/14/93
	BL	5,592,510	01/07/97	Van Brunt et al.	375	220	03/29/94
	BM	5,619,544	04/08/97	Lewis et al.	375	377	02/27/96
	BN	5,754,548	05/19/98	Hoekstra et al.	370	402	02/21/97
	BO	5,781,028	07/14/98	Decuir	326	30	06/21/96
	BP	5,796,042	08/18/98	Pope	174	102SP	06/21/96
	BQ	5,808,660	09/15/98	Sekine et al.	348	8	09/04/96
	BR	5,881,249	03/09/99	Reasoner	395	281	07/31/95
	BS	5,945,631	08/31/99	Henrikson et al.	174	34	09/16/96

## OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

	BT	"1394 200 Mb/s PHYSICAL Layer Transceiver," IBM Microelectronics, Product Data Sheet and Application Notes, Version 1.4, 3/14/96.
	BU	"IEEE 1394-1995 TRIPLE CABLE TRANSRECEIVER/ ARBITER," Texas Instruments, TSB21LV03, Product Preview, Revision 0.99, 3/19/96.
	BV	"P1394 Standard for a High Performance Serial Bus," IEEE P1394 Draft 8.0v2, July 7, 1995.
	BW	Tensolite Company product specification, part number 20470/9J207X-4(LD).
	BX	Tensolite Company product specification, part number 18480/9J207X-4(LD).
	BY	Tensolite Company product specification, part number 24443/9B048X-4(LD), 6/3/93.
	BZ	Tensolite Company product specification, part number 24443/9C062X-4(LD), 3/17/93.
	CA	Craig Theorin, "High speed serial links benefit from advanced cabling," 10/26/95.
	CB	Raychem specification control drawing, part number EPD-RWC-13458, 8/7/95.
	CC	Raychem specification control drawing, part number 82A0111, 9/10/95, page 1 of 2.
	CD	Michael Teener et al., "A Bus on a Diet - The Serial Bus Alternative, An Introduction to the P1394 High performance Serial Bus" Apple Computer, Inc. Santa Clara, CA, Pub. Date.: 02/24/92, pgs. 316-321.
	CF	"The IEEE-1394 High Speed Serial Bus," R.H.J. Bloks, Philips Journal Of Research, Vol.50, No. 1/2, pp. 209-216, 1996.
	CF	P1394a Draft Standard For A High Performance Serial Bus (Supplement), P1394a Draft 2.0 March 15, 1998.
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